806-049 VersaLink PCB Jam-Nut Receptacle

PART NUMBER DEVELOPMENT					
Sample Part Number	**806-049**	**-ME**	**11-4V**	**S**	**A**
Series / Basic No.	**806-049** PCB Jam-Nut Receptacle				
Material / Finish	**ME** = Aluminum alloy, electroless nickel (96 hour salt spray) **MT** = Aluminum alloy, nickel-PTFE **NF** = Aluminum alloy, cad/O.D. over electroless nickel **ZR** = Aluminum alloy, zinc nickel, black (trivalent chromium) **Z1** = Aluminum alloy, stainless steel passivated **ZL** = Aluminum alloy, electro-deposited nickel				
Insert Layout	See Insert Arrangement Table (See page 13–14 for layouts)				
Contact Gender	**P** = Pin **S** = Socket				
Polarization	**A**, **B**, **C**, **D**, **E**, **F**; see Polarization Table				

MATERIALS / FINISH

- Shell: see part number development
- Pin contact: copper alloy / 50µ gold over nickel
- Socket contact: copper alloy with stainless steel hood (22HD and 20HD) / 50µ gold over nickel, contact hood passivate
- Insulators: high-grade rigid dielectric
- Seals: fluorosilicone/silicone blend
- Potting: epoxy

PERFORMANCE SPECIFICATIONS

- Current rating for 20HD and 22HD:
  20 AWG: 7.5 amps
  22 AWG: 5 amps
- DWV:
  20HD: 1800 VAC at sea level
  22HD: 1300 VAC at sea level
  VersaLink: 500 VAC at sea level
- Insulation resistance:
  5000 MΩ min. at ambient temperature
  1000 MΩ min. at elevated temperature
- Operating temperature: -65° to +200° C

NOTES

1. Integral PCB stand off 4x 4-40 UNC, .156 minimum thread and locating post.
2. Connector is backfilled with epoxy, contacts are non-removable.
3. Receptacle connector mates with 806-044 of the same arrangement, polarization, and opposite gender.
4. Refer to Refer to Series 806 PCB footprints on page 38.
5. Red band indicates full mate.

INSERT ARRANGEMENT			
SHELL SIZE-LAYOUT	NUMBER OF CONTACTS		
	VersaLink	22HD	20HD
8-1V[1]	1		
9-2V[2]	2		
10-2V2	2		2
11-4V[3]	4		
11-2V9[4]	2	9	
12-4V4	4	4	
14-6V7	6	7	
14-4V15[5]	4	15	
16-10V4	10	4	
16-8V10	8	10	
18-14V4	14	4	
18-8V31	8	31	
20-18V8	18	8	
22-25V4	25	4	
22-16V19	16	19	
24-32V	32		
24-16V66	16	66	

Support for:
1. 10G SPE
2. SATA 3
3. 40G Ethernet
4. USB 3.1 Gen 2
5. USB 3.2, USB 4, HDMI, DP, DVI Single Link

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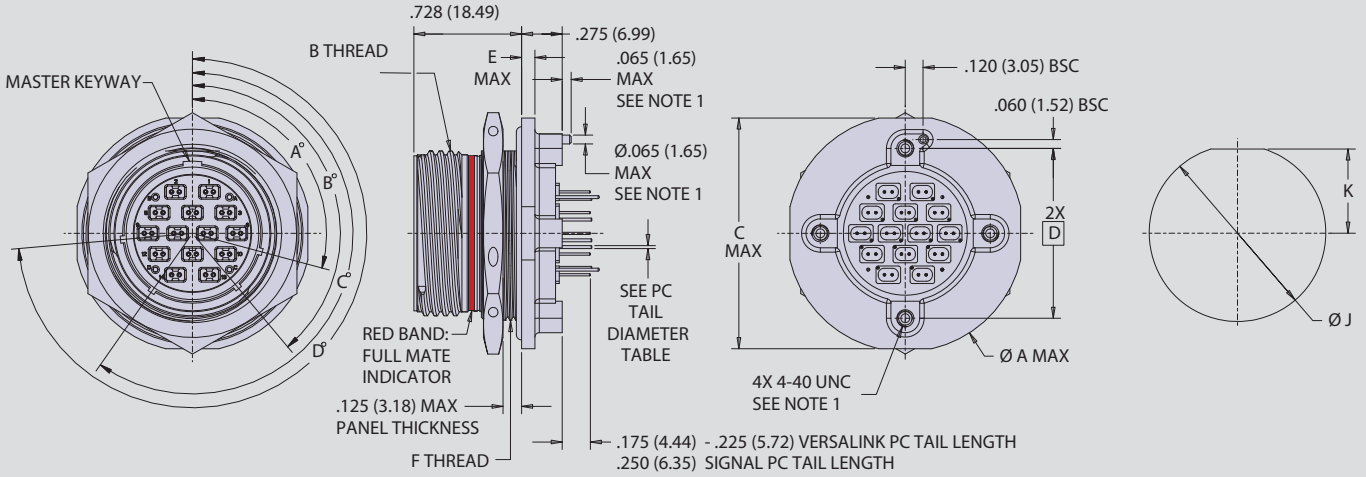
806-049 VersaLink PCB Jam-Nut Receptacle



DIMENSIONS						
SHELL SIZE	ØA MAX	B MATING THREAD	C MAX	D	E MAX	F THREAD
8	0.980 (24.89)	.5000-.067P-.2L-TS-2B	0.920 (23.37)	0.534 (13.56)	0.100 (2.54)	M15 X 1-6g.100R
9	1.040 (26.42)	.5625-.067P-.2L-TS-2B	0.980 (24.89)	0.579 (14.71)	0.100 (2.54)	M16 X 1-6g.100R
10	1.110 (28.19)	.6250-.067P-.2L-TS-2B	1.050 (26.67)	0.679 (17.25)	0.100 (2.54)	M18 X 1-6g.100R
11	1.160 (29.46)	.6875-.067P-.2L-TS-2B	1.110 (28.19)	0.734 (18.64)	0.100 (2.54)	M19 X 1-6g.100R
12	1.230 (31.24)	.7500-.067P-.2L-TS-2B	1.170 (29.72)	0.804 (20.42)	0.100 (2.54)	M21 X 1-6g.100R
14	1.360 (34.54)	.8750-.067P-.2L-TS-2B	1.320 (33.53)	0.891 (22.63)	0.100 (2.54)	M24 X 1-6g.100R
16	1.515 (38.48)	1.0000-.067P-.2L-TS-2B	1.444 (36.68)	1.049 (26.64)	0.100 (2.54)	M27 X 1-6g.100R
18	1.610 (40.89)	1.1250-.067P-.2L-TS-2B	1.570 (39.88)	1.148 (29.16)	0.100 (2.54)	M30 X 1-6g.100R
20	1.850 (46.99)	1.2500-.067P-.2L-TS-2B	1.760 (44.70)	1.252 (31.80)	0.128 (3.25)	M34 X 1-6g.100R
22	2.010 (51.05)	1.3750-.067P-.2L-TS-2B	1.913 (48.59)	1.369 (34.77)	0.128 (3.25)	M37 X 1-6g.100R
24	2.195 (55.75)	1.5000-.067P-.2L-TS-2B	2.070 (52.58)	1.509 (38.33)	0.128 (3.25)	M41 X 1-6g.100R

POLARIZATION				
POS	A	B	C	D
A	105	140	215	265
B	102	170	248	305
C	80	150	230	295
D	68	140	205	275
E	64	155	234	304
F	72	120	200	298

CUTOUT PANEL DIMENSIONS		
SHELL SIZE	ØJ	K
8	0.601 (15.27)	0.256 (6.50)
9	0.640 (16.26)	0.287 (7.29)
10	0.719 (18.26)	0.318 (8.08)
11	0.759 (19.28)	0.350 (8.89)
12	0.837 (21.26)	0.381 (9.68)
14	0.955 (24.26)	0.443 (11.25)
16	1.073 (27.25)	0.505 (12.83)
18	1.192 (30.28)	0.568 (14.43)
20	1.349 (34.26)	0.630 (16.00)
22	1.467 (37.26)	0.693 (17.60)
24	1.624 (41.25)	0.775 (19.69)

PC TAIL DIAMETERS	
CONTACT	MAX PC TAIL DIAMETER
#20	0.030 (0.76)
#22	0.020 (0.51)
VL SIGNAL	0.017 (0.43)
VL GROUND	0.022 (0.56)

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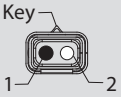
REV. 11.10.25

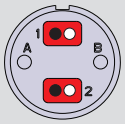
Insert Arrangements

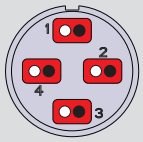
VERSALINK PIN INTERFACE SHOWN, SOCKET INTERFACE MIRRORED

Versalink Pin For Reference



Insert Arrangement
Mating Face View of Pin Contacts

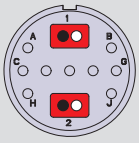


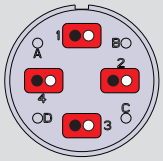


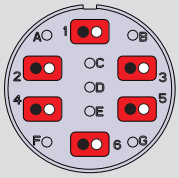
Cavities are upside down.

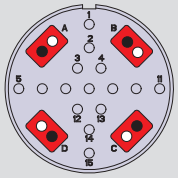
Series 806	8-1V	9-2V	10-2V2		11-4V
Contact Size	VersaLink	VersaLink	VersaLink	20HD	VersaLink
No. of Contacts	1	2	2	2	4
Protocol(s) Supported	10GBase-T1 Ethernet	SATA 3			40G Ethernet

Insert Arrangement
Mating Face View of Pin Contacts



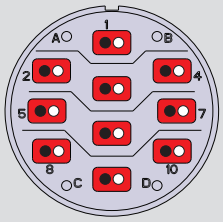


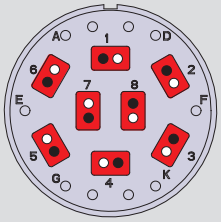


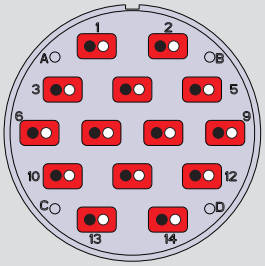


Series 806	11-2V9		12-4V4		14-6V7		14-4V15*	
Contact Size	VersaLink	22HD	VersaLink	22HD	VersaLink	22HD	VersaLink	22HD
No. of Contacts	2	9	4	4	6	7	4	15
Protocol(s) Supported	USB 3.1 Gen 2						USB 3.1 Gen 2 Type C, USB 3.2, USB4, HDMI, DisplayPort, DVI Single Link	

Insert Arrangement
Mating Face View of Pin Contacts







Series 806	16-10V4		16-8V10*		18-14V4	
Contact Size	VersaLink	22HD	VersaLink	22HD	VersaLink	22HD
No. of Contacts	10	4	8	10	14	4

* For arrangements with angled cavities, contact keyway always faces away from center.

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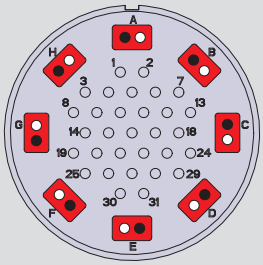
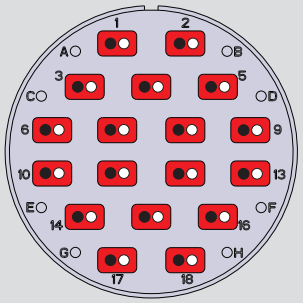
Insert Arrangements

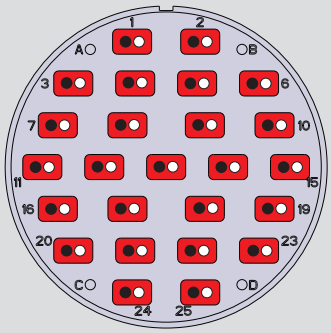
Insert Arrangement
Mating Face View of Pin Contacts

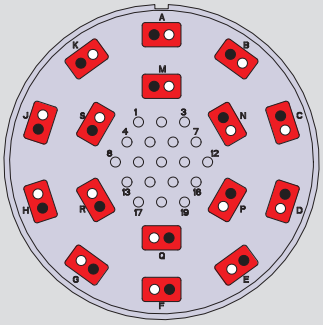




Series 806	18-8V31*		20-18V8	
Contact Size	VersaLink	22HD	VersaLink	22HD
No. of Contacts	8	31	18	8

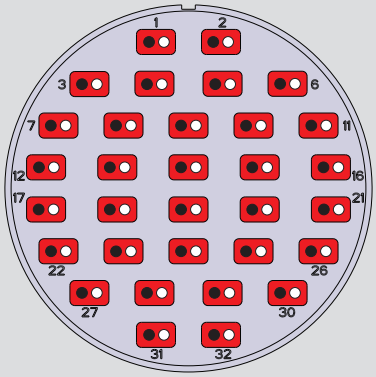
Insert Arrangement
Mating Face View of Pin Contacts

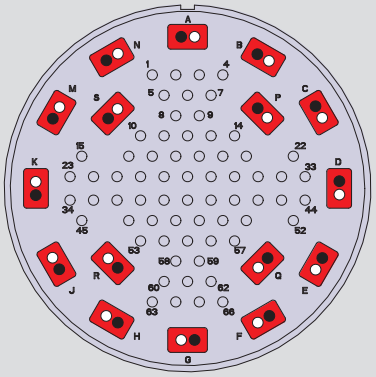




Series 806	20-25V4		22-16V19*	
Contact Size	VersaLink	22HD	VersaLink	22HD
No. of Contacts	25	4	16	19

Insert Arrangement
Mating Face View of Pin Contacts





Series 806	24-32V	24-16V66*	
Contact Size	VersaLink	VersaLink	22HD
No. of Contacts	32	16	66

* For arrangements with angled cavities, contact keyway always faces away from center.

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Performance Specifications

STANDARD MATERIALS AND FINISHES		
DESCRIPTION	**MATERIAL**	**FINISH**
Pin Contact	Copper alloy	50 microinches gold over nickel
Socket Contact	Copper alloy, with stainless steel hood (22HD and 20HD contacts only)	50 microinches gold over nickel Contact hood: passivate
Insulators	High-grade rigid dielectric	None
Seals	Fluorosilicone/silicone blend, blue	None
EMI Spring	Beryllium copper	Nickel
Shell, Coupling Nut, Jam-nut	Aluminum alloy or stainless steel	See how-to-order tables for finish options
Contact Retention Clip	Beryllium copper	Nickel
Anti-Decoupling Ratchet Spring	Stainless steel	Passivate

PERFORMANCE SPECIFICATION

TEST DESCRIPTION	REQUIREMENT	PROCEDURE
Dielectric withstanding voltage at sea level	Contact Size / Altitude / Voltage: 20HD — Sea level — 1800 22HD — Sea level — 1300	MIL-DTL-38999M para. 4.5.11.1 EIA-364-20 Method A 2 mA maximum leakage current Unmated pairs
Dielectric withstanding voltage at altitude	Contact Size / Altitude / Voltage: 20HD — 50,000 ft — 1000 20HD — 70,000 ft — 1000 20HD — 100,000 ft — 1000 22HD — 50,000 ft — 800 22HD — 70,000 ft — 800 22HD — 100,000 ft — 800	MIL-DTL-38999M para. 4.5.11.2 EIA-364-20 Method A 2 mA maximum leakage current Mated pairs
Insulation resistance at ambient temperature	5000 megohms minimum	MIL-DTL-38999M Para. 4.5.10.1 EIA-364-21
Insulation resistance at elevated temperature	1000 megohms minimum	MIL-DTL-38999M Para. 4.5.10.2 EIA-364-21
Contact resistance at 25°C, 22HD and 20HD crimp contacts	Wire Size / Test Current Amperes / Maximum Voltage Drop (millivolts) Initial / After Conditioning: 20 — 7.5 — 55 — 66 22 — 5 — 73 — 88 24 — 3 — 45 — 54 26 — 2 — 52 — 63 28 — 1.5 — 54 — 65 30 — 1 — 60 — 73	AS39029C Para. 4.7.5 EIA-364-06 Silver-plated wire
Contact resistance at 200° C, 22HD and 20HD crimp contacts	Wire Size / Test Current Amperes / Maximum Voltage Drop (millivolts): 20 — 7.5 — 94 22 — 5 — 125 24 — 3 — 77 26 — 2 — 89 28 — 1.5 — 92 30 — 1 — 103	AS39029C Para. 4.7.5 EIA-364-06 Silver-plated wire
Low level contact resistance, 22HD and 20HD crimp contacts	Wire Size / Maximum Contact Resistance (milliohms) Initial Values / After Conditioning: 20 — 9 — 11 22 — 15 — 17 24 — 20 — 23 26 — 31 — 38 28 — 50 — 60 30 — 75 — 88	AS39029C Para. 4.7.4 EIA-364-23 Silver plated wire

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Performance Specifications

PERFORMANCE SPECIFICATION

TEST DESCRIPTION	REQUIREMENT	PROCEDURE
Shell-to-shell conductivity	Finish Code / Shell Matl/Fin / Millivolt Drop (mV) NF — Al/OD Cad — 2.5 MT — Al/Ni-PTFE — 2.5 ME — Al/EN — 1.0 ZR — Al/Zn-Ni — 2.5 Z1 — SST/pass. — 10.0 ZL — SST/Ni — 1.0	MIL-DTL-38999M Para. 4.5.25 EIA-364-83
Backshell shield braid to shell conductivity	Finish Code / Shell Matl/Fin / Millivolt Drop (mV) NF — Al/OD Cad — 5.0 MT — Al/Ni-PTFE — 5.0 ME — Al/EN — 3.5 ZR — Al/Zn-Ni — 5.0 Z1 — SST/pass. — 15.0 ZL — SST/Ni — 3.5	MIL-DTL-38999M Para. 4.5.25.1 EIA-364-83
Indirect lightning strike	No evidence of damage which could impair proper functioning. Connectors shall meet shell-to-shell conductivity, DWV and coupling torque.	MIL-DTL-38999M Para. 4.5.47 EIA-364-75 10,000 Amps peak current
EMI shielding	Freq. MHz / Leakage Attenuation, (dB) minimum: Electroless Nickel Finish / Cadmium, Nickel-PTFE, Zinc-Nickel Finish 100 — 90 — 90 200 — 88 — 88 300 — 88 — 88 400 — 87 — 87 800 — 85 — 85 1,000 — 85 — 85 1,500 — 76 — 69 2,000 — 70 — 65 3,000 — 69 — 61 4,000 — 68 — 58 6,000 — 66 — 55 10,000 — 65 — 50	MIL-DTL-38999M Para. 4.5.28
Durability	No evidence of damage which could impair proper functioning following 500 cycles of mating and unmating.	MIL-DTL-38999M Para. 4.5.8 EIA-364-09
Coupling and uncoupling torque	Shell size / Maximum Engagement lbs-inch. / Minimum Disengagement in-lb 8 — 8 — 2 9 — 8 — 2 10 — 12 — 2 11 — 12 — 2 12 — 12 — 2 14 — 16 — 2 16 — 20 — 3 18 — 24 — 3 20 — 28 — 3 22 — 32 — 5 24 — 36 — 5	MIL-DTL-38999M Para. 4.5.7 EIA-364-114
Insert retention	100 pounds per square inch, 25 pound minimum force	MIL-DTL-38999M Para. 4.5.12 EIA-364-35
External bend moment	Shell size / Inch-Pound 8 — 100 9 — 100 10 — 100 11 — 200 12 — 300 14 — 400 16 — 500 18 — 600 20 — 700 22 — 800 24 — 900	MIL-DTL-38999M Para. 4.5.16 EIA-364-43

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Performance Specifications

PERFORMANCE SPECIFICATION		
TEST DESCRIPTION	**REQUIREMENT**	**PROCEDURE**
Contact retention	Contact size: 22HD — Pounds ± 10 percent: 10 Contact size: 20HD — Pounds ± 10 percent: 10	MIL-DTL-38999M Para. 4.5.20.1 EIA-364-29
Magnetic permeability	2 µ maximum	MIL-DTL-38999M Para. 4.5.48 EIA-364-54
Contact engaging /separation force	Contact forces shall meet AS39029 Table 9 requirements	AS39029C Para. 4.7.6 EIA-364-37
Temperature cycling (thermal shock)	No evidence of damage detrimental to the function of the connector	MIL-DTL-38999M Para. 4.5.4 EIA-364-32 Mated connectors, -65° C to +200° C
Random vibration, ambient temperature, 49g rms	No discontinuities of 1 microsecond or longer	MIL-DTL-38999M Para. 4.5.23.2.4 EIA-364-28 Test Condition V
Sine vibration, 60g	No discontinuities of 1 microsecond or longer	MIL-DTL-38999M Para. 4.5.23.2.1 with Figure 24 accessory load 12 hours in each of 3 axes 4 hours at ambient, 4 hours at -55° C, 4 hours at +200° C
Mechanical shock, 300g	No discontinuities of 1 microsecond or longer	MIL-DTL-38999M Para. 4.5.24.1 EIA-364-27
Humidity, cyclic	Meet DWV and IR test	MIL-DTL-38999M Para. 4.5.26 EIA-364-31 Method 4 10 cycles, 10 days, 25 – 65°C 80 – 100% RH
Ozone exposure	No evidence of damage detrimental to the function of the connector	MIL-DTL-38999M Para. 4.5.29 EIA-364-14
Thermal vacuum outgassing	All nonmetallic materials shall not exceed 1.0% Total Mass Loss and 0.1% Total Volatile Condensable Materials. Applicable only to connectors that have been subjected to optional thermal vacuum outgassing.	MIL-DTL-38999M Para. 4.5.42 ASTM E595

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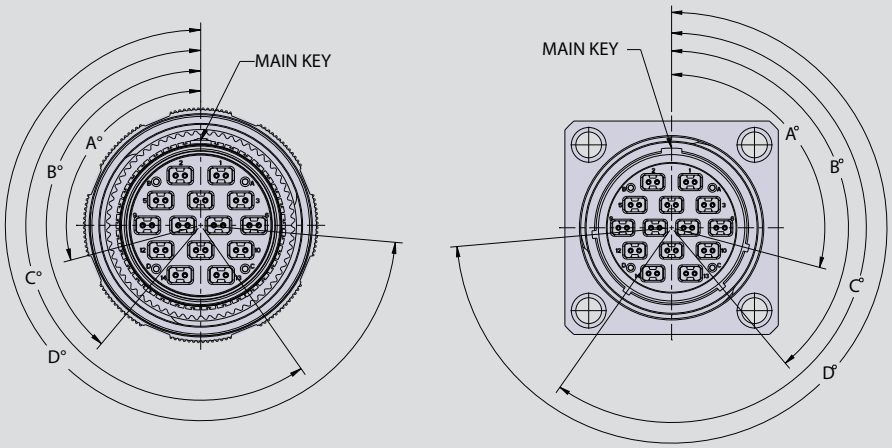
Polarization and Connector Torque Values

SERIES 806 CONNECTOR POLARIZATION



POLARIZATION				
POS	A	B	C	D
A	105	140	215	265
B	102	170	248	305
C	80	150	230	295
D	68	140	205	275
E	64	155	234	304
F	72	120	200	298

SERIES 806 CONNECTOR TORQUE VALUES

SERIES 806 RECOMMENDED TORQUE VALUES FOR HIGH VIBRATION ENVIRONMENTS						
	Standard In-Lbf		Composite In-Lbf		High Vibration In-Lbf	
Shell Size	Min	Max	Min	Max	Min	Max
8	42	52	20	25	57	68
9	51	61	20	30	66	76
10	63	73	20	30	87	97
11	71	81	20	30	103	113
12	79	89	25	35	107	117
14	105	115	25	35	121	131
16	111	121	25	35	139	149
18	111	121	30	40	139	149
20	111	121	30	40	139	149
22	131	141	35	45	175	185
24	131	141	35	45	175	185

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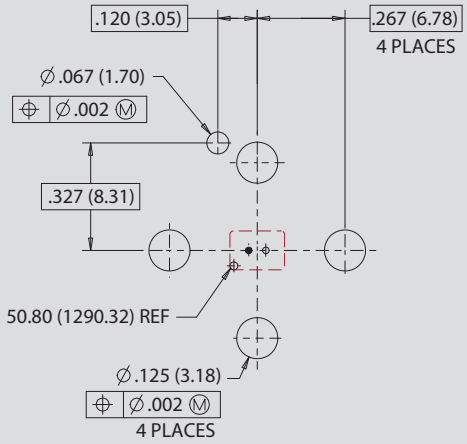
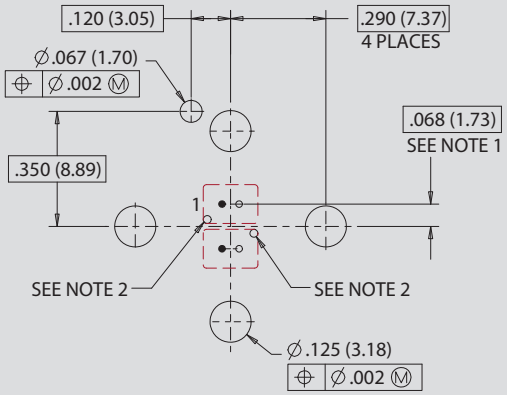
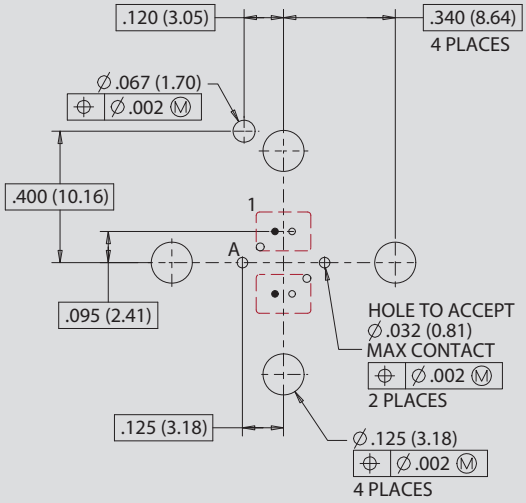
PCB Footprints



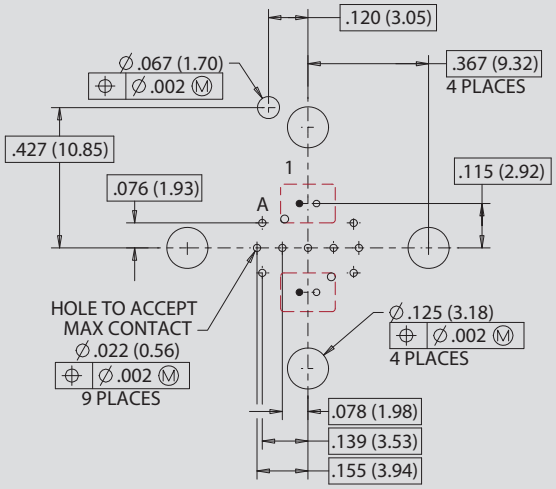
Arrangement No. 8-1V
1X VersaLink



Arrangement No. 9-2V
2X VersaLink



Arrangement No. 10-2V2
2X VersaLink
2X 20HD
See Note 1 and 2

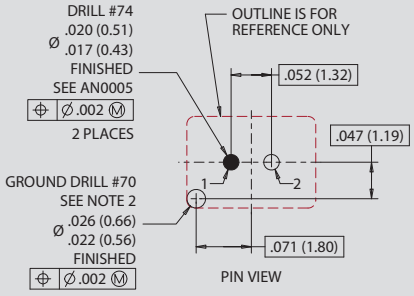


Arrangement No. 11-2V9
2X VersaLink
9X 22HD
See Note 1 and 2

NOTES

1. Location given for each pair is the center of each pair layout.
2. Ground leg location varies within each arrangment. Ground leg location has no impact on pinout. Ground leg stays in same location for both pin and sockets. Pin 1 is indicated (filled) on all arrangements.
3. See AN0005 for proper PCB design rules.
4. Pin cavity and inner contact locations shown. Socket cavity and inner contact locations are mirrored. Ground leg location does not change.

VERSALINK PAIR LAYOUT (SEE NOTE 1)



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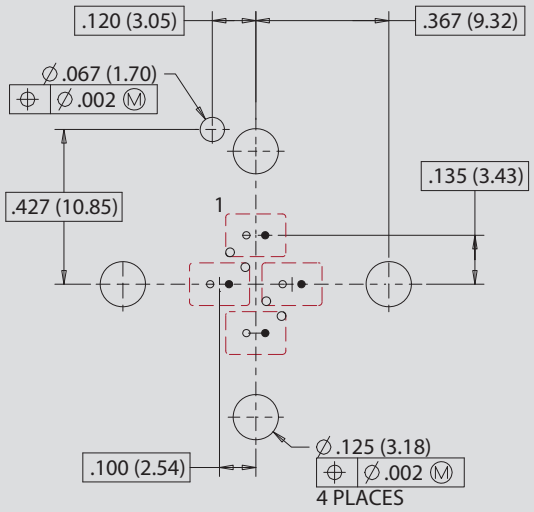
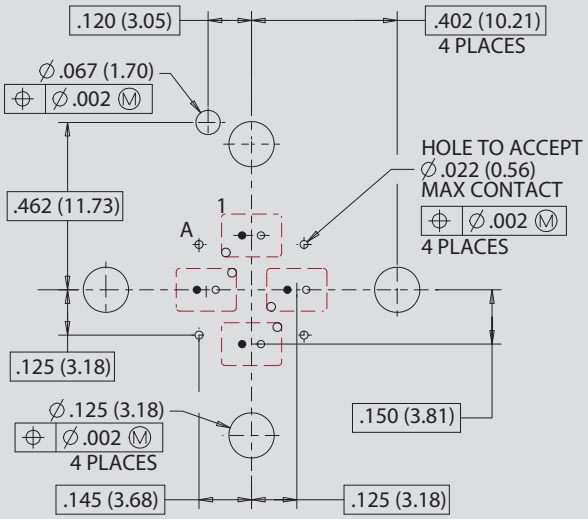
Arrangement No. 11-4V
4X VersaLink
See Note 1 and 2
Cavities are upside down.



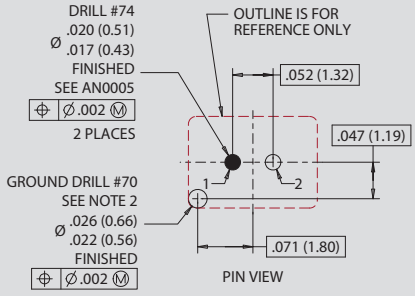
Arrangement No. 12-4V4
4X VersaLink
4X 22HD
See Note 1 and 2



NOTES

1. Location given for each pair is the center of each pair layout.
2. Ground leg location varies within each arrangment. Ground leg location has no impact on pinout. Ground leg stays in same location for both pin and sockets. Pin 1 is indicated (filled) on all arrangements.
3. See AN0005 for proper PCB design rules.
4. Pin cavity and inner contact locations shown. Socket cavity and inner contact locations are mirrored. Ground leg location does not change.

VERSALINK PAIR LAYOUT (SEE NOTE 1)



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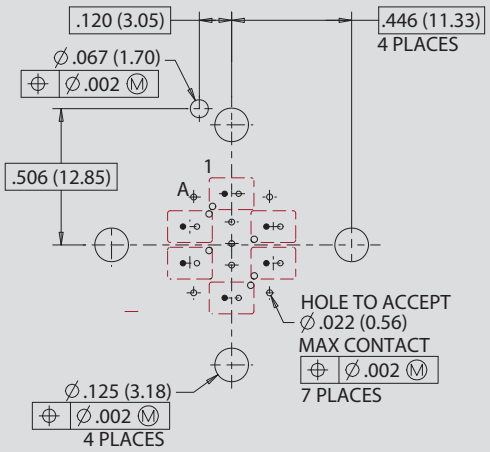
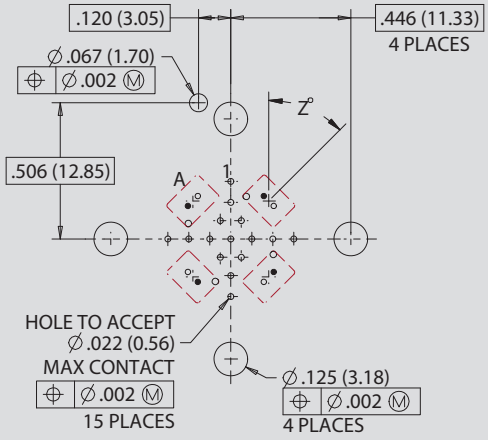
PCB Footprints

Arrangement No. 14-6V7
6X VersaLink
7X 22HD
See Note 1 and 2



CONTACT POSITIONS		
POS	X	Y
1	.000 (0.00)	.185 (4.70)
2	-.155 (-3.94)	.068 (1.73)
3	.155 (3.94)	.068 (1.73)
4	-.155 (-3.94)	.068 (1.73)
5	.1585 (4.03)	-.068 (-1.73)
6	.000 (0.00)	-.185 (-4.70)
A	.141 (3.58)	.178 (4.52)
B	-.141 (-3.58)	.1787 (4.54)
C	.000 (0.00)	.085 (2.16)
D	.000 (0.00)	.005 (0.13)
E	.000 (0.00)	.075 (1.91)
F	.141 (3.58)	-.178 (-4.52)
G	-.141 (-3.58)	-.178 (-4.52)

Arrangement No. 14-4V15
2X VersaLink
4X 20HD
15X 22HD
See Note 1 and 2

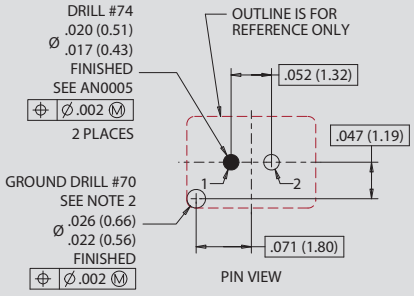


CONTACT POSITIONS			
POS	X	Y	Z° ±0.5°
A	-.141 (-3.58)	.141 (3.58)	315
B	.141 (3.58)	.141 (3.58)	45
C	.141 (3.58)	-.141 (-3.58)	135
D	-.141 (-3.58)	-.141 (-3.58)	225
1	.000 (0.00)	.214 (5.44)	
2	.000 (0.00)	.136 (3.45)	
3	-.039 (-0.99)	.068 (1.73)	
4	.039 (0.99)	.068 (1.73)	
5	-.234 (-5.94)	.000 (0.00)	
6	-.156 (-3.96)	.000 (0.00)	
7	-.078 (-1.98)	.000 (0.00)	
8	.000 (0.00)	.000 (0.00)	
9	.078 (1.98)	.000 (0.00)	
10	.156 (3.96)	.000 (0.00)	
11	.234 (5.94)	.000 (0.00)	
12	-.039 (-0.99)	-.068 (-1.73)	
13	.039 (0.99)	-.068 (-1.73)	
14	.000 (0.00)	-.136 (-3.45)	
15	.000 (0.00)	-.214 (-5.44)	

NOTES

1. Location given for each pair is the center of each pair layout.
2. Ground leg location varies within each arrangment. Ground leg location has no impact on pinout. Ground leg stays in same location for both pin and sockets. Pin 1 is indicated (filled) on all arrangements.
3. See AN0005 for proper PCB design rules.
4. Pin cavity & inner contact locations shown. Socket cavity and inner contact locations are mirrored. Ground leg location does not change.

VERSALINK PAIR LAYOUT (SEE NOTE 1)



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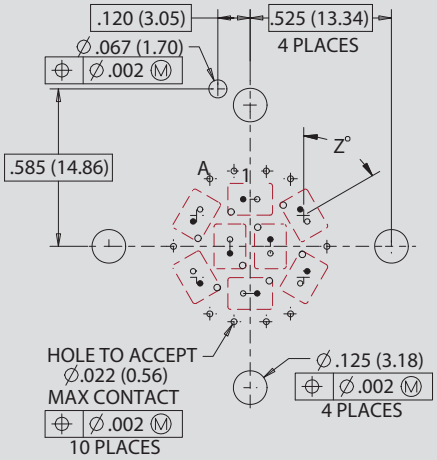
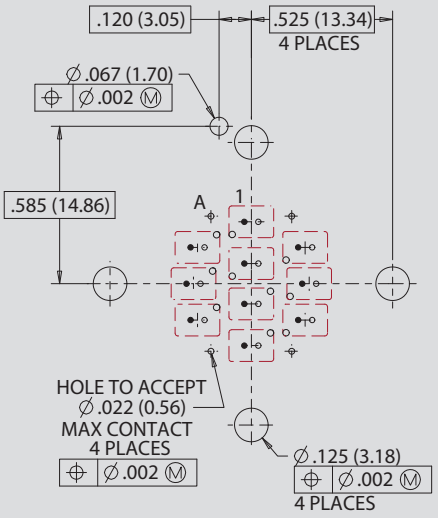
Arrangement No. 16-8V10
8X VersaLink
10X 22HD
See Note 1 and 2



CONTACT POSITIONS			
POS	X	Y	Z° ±0.5°
1	.000 (0.00)	.175 (4.45)	0
2	.199 (5.05)	.115 (2.92)	60
3	.199 (5.05)	-.115 (-2.92)	120
4	.000 (0.00)	-.175 (-4.45)	180
5	-.199 (-5.05)	-.115 (-2.92)	240
6	-.199 (-5.05)	.115 (2.92)	300
7	-.075 (-1.91)	.000 (0.00)	270
8	.075 (1.91)	.000 (0.00)	90
A	-.150 (-3.81)	.251 (6.38)	
B	-.060 (-1.52)	.280 (7.11)	
C	.060 (1.52)	.280 (7.11)	
D	.150 (3.81)	.251 (6.38)	
E	-.285 (-7.24)	.000 (0.00)	
F	.285 (7.24)	.000 (0.00)	
G	-.150 (-3.81)	-.251 (-6.38)	
H	-.060 (-1.52)	-.281 (-7.14)	
J	.060 (1.52)	-.281 (-7.14)	
K	.150 (3.81)	.251 (6.38)	

Arrangement No. 16-10V4
10X VersaLink
4X 22HD
See Note 1 and 2

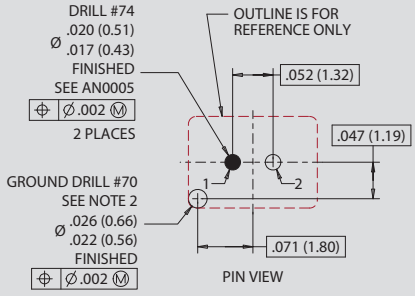


CONTACT POSITIONS		
POS	X	Y
1	.000 (0.00)	.230 (5.84)
2	-.200 (-5.08)	.135 (3.43)
3	.000 (0.00)	.075 (1.91)
4	.200 (5.08)	.135 (3.43)
5	-.215 (-5.46)	.000 (0.00)
6	.000 (0.00)	-.075 (-1.91)
7	.215 (5.46)	.000 (0.00)
8	-.200 (-5.08)	-.135 (-3.43)
9	.000 (0.00)	-.230 (-5.84)
10	.200 (5.08)	.135 (3.43)
A	-.150 (-3.81)	.251 (6.38)
B	.150 (3.81)	.251 (6.38)
C	-.150 (-3.81)	-.251 (-6.38)
D	.150 (3.81)	-.251 (-6.38)

NOTES

1. Location given for each pair is the center of each pair layout.
2. Ground leg location varies within each arrangment. Ground leg location has no impact on pinout. Ground leg stays in same location for both pin and sockets. Pin 1 is indicated (filled) on all arrangements.
3. See AN0005 for proper PCB design rules.
4. Pin cavity and inner contact locations shown. Socket cavity and inner contact locations are mirrored. Ground leg location does not change.

VERSALINK PAIR LAYOUT (SEE NOTE 1)



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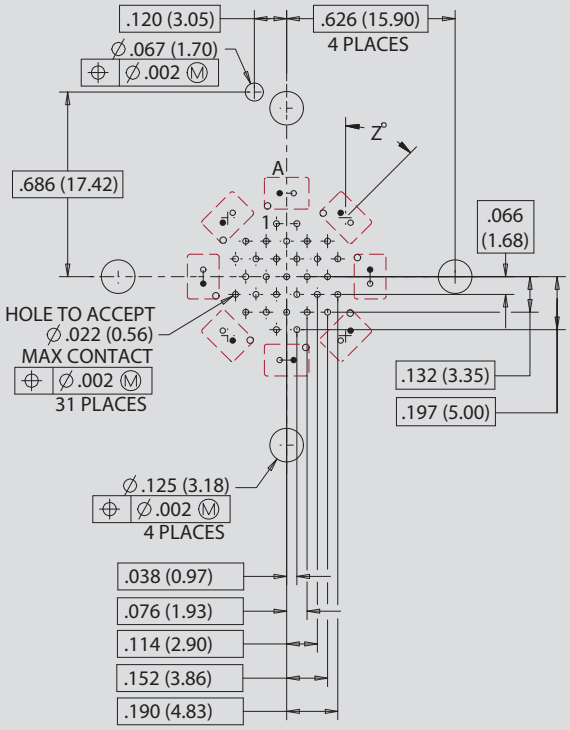
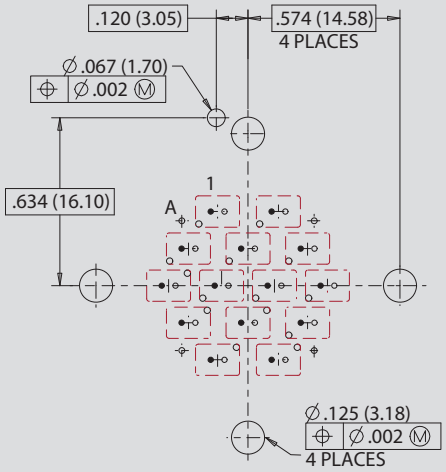
PCB Footprints

Arrangement No. 16-8V31
8X VersaLink
31X 22HD
See Note 1 and 2



CONTACT POSITIONS			
POS	X	Y	Z° ±0.5°
A	.000 (0.00)	.310 (7.87)	0
B	.219 (5.56)	.219 (5.56)	45
C	.310 (7.87)	.000 (0.00)	90
D	.219 (5.56)	-.219 (-5.56)	135
E	.000 (0.00)	-.310 (-7.87)	180
F	-.219 (-5.56)	-.219 (-5.56)	225
G	-.310 (-7.87)	.000 (0.00)	270
H	-.219 (-5.56)	.219 (5.56)	315

Arrangement No. 18-18V8
18X VersaLink
8X 22HD
See Note 1 and 2

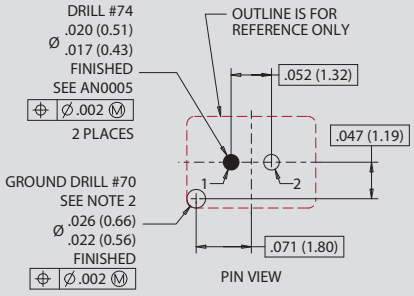


CONTACT POSITIONS		
POS	X	Y
1	-.115 (-2.92)	.280 (7.11)
2	.115 (2.92)	.280 (7.11)
3	-.225 (-5.72)	.140 (3.56)
4	.000 (0.00)	.140 (3.56)
5	.225 (5.72)	.140 (3.56)
6	-.300 (-7.62)	.000 (0.00)
7	-.100 (-2.54)	.000 (0.00)
8	.100 (2.54)	.000 (0.00)
9	.300 (7.62)	.000 (0.00)
10	-.225 (-5.72)	-.140 (-3.56)
11	.000 (0.00)	-.140 (-3.56)
12	.225 (5.72)	-.140 (-3.56)
13	-.115 (-2.92)	-.280 (-7.11)
14	.115 (2.92)	-.280 (-7.11)
A	-.250 (-6.35)	.245 (6.22)
B	.250 (6.35)	.245 (6.22)
C	-.250 (-6.35)	-.245 (-6.22)
D	.250 (6.35)	-.245 (-6.22)

NOTES

1. Location given for each pair is the center of each pair layout.
2. Ground leg location varies within each arrangment. Ground leg location has no impact on pinout. Ground leg stays in same location for both pin and sockets. Pin 1 is indicated (filled) on all arrangements.
3. See AN0005 for proper PCB design rules.
4. Pin cavity and inner contact locations shown. Socket cavity and inner contact locations are mirrored. Ground leg location does not change.

VERSALINK PAIR LAYOUT (SEE NOTE 1)



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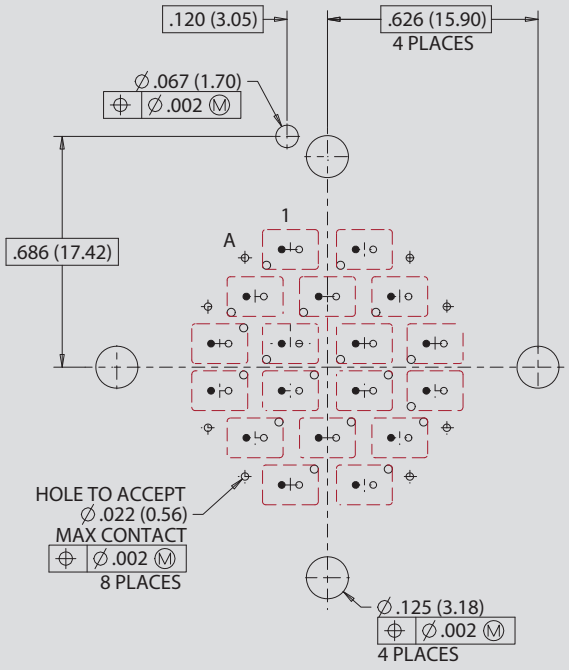
Arrangement No. 20-18V8
18X VersaLink
8X 22HD
See Note 1 and 2

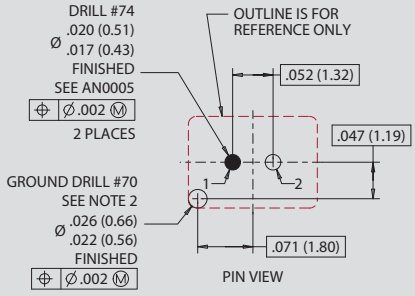


CONTACT POSITIONS		
POS	X	Y
1	-.110 (-2.79)	.350 (8.89)
2	.110 (2.79)	.350 (8.89)
3	-.215 (-5.46)	.210 (5.33)
4	.000 (0.00)	.210 (5.33)
5	.215 (5.46)	.210 (5.33)
6	-.320 (-8.13)	.070 (1.78)
7	-.110 (-2.79)	.070 (1.78)
8	.110 (2.79)	.070 (1.78)
9	.320 (8.13)	.070 (1.78)
10	-.320 (-8.13)	-.070 (-1.78)
11	-.110 (-2.79)	-.070 (-1.78)
12	.110 (2.79)	-.070 (-1.78)
13	.320 (8.13)	-.070 (-1.78)
14	-.215 (-5.46)	-.210 (-5.33)
15	.000 (0.00)	-.210 (-5.33)
16	.215 (5.46)	-.210 (-5.33)
17	-.110 (-2.79)	-.350 (-8.89)
18	.110 (2.79)	-.350 (-8.89)
A	-.245 (-6.22)	.325 (8.26)
B	.245 (6.22)	.325 (8.26)
C	-.355 (-9.02)	.180 (4.57)
D	.355 (9.02)	.180 (4.57)
E	-.355 (-9.02)	-.180 (-4.57)
F	.355 (9.02)	-.180 (-4.57)
G	-.245 (-6.22)	-.325 (-8.26)
H	.245 (6.22)	-.325 (-8.26)

NOTES

1. Location given for each pair is the center of each pair layout.
2. Ground leg location varies within each arrangment. Ground leg location has no impact on pinout. Ground leg stays in same location for both pin and sockets. Pin 1 is indicated (filled) on all arrangements.
3. See AN0005 for proper PCB design rules.
4. Pin cavity and inner contact locations shown. Socket cavity and inner contact locations are mirrored. Ground leg location does not change.

VERSALINK PAIR LAYOUT (SEE NOTE 1)



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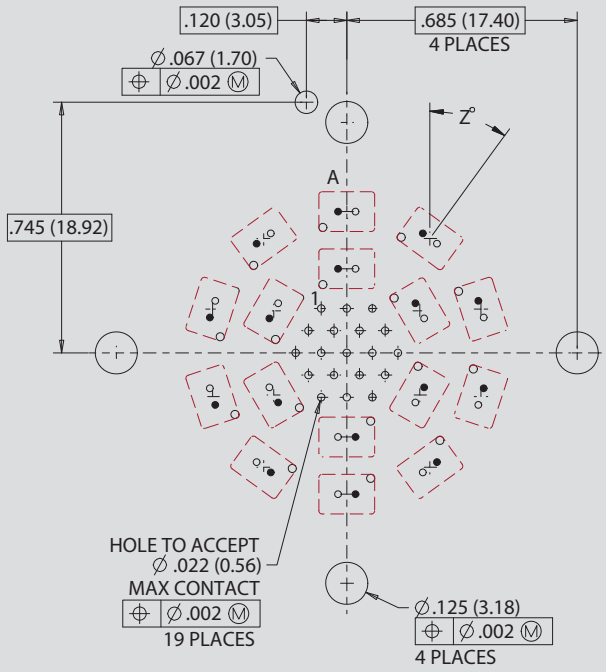
PCB Footprints

Arrangement No.
22-16V19
16X VersaLink
19X 22HD
See Note 1 and 2

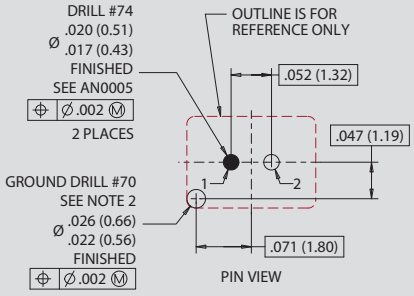


CONTACT POSITIONS			
POS	X	Y	Z° ±0.5°
A	.000 (0.00)	.420 (10.67)	0
B	.247 (6.27)	.340 (8.64)	36
C	.399 (10.13)	.130 (3.30)	72
D	.399 (10.13)	-.130 (-3.30)	108
E	.247 (6.27)	-.340 (-8.64)	144
F	.000 (0.00)	.420 (10.67)	180
G	-.247 (-6.27)	.340 (8.64)	216
H	-.399 (-10.13)	-.130 (-3.30)	252
J	-.399 (-10.13)	.130 (3.30)	288
K	-.247 (-6.27)	.340 (8.64)	324
M	.000 (0.00)	.250 (6.35)	0
N	.217 (5.51)	.125 (3.18)	60
P	.217 (5.51)	-.125 (-3.18)	120
Q	.000 (0.00)	-.250 (-6.35)	180
R	-.217 (-5.51)	-.125 (-3.18)	240
S	-.217 (-5.51)	.125 (3.18)	300
1	.076 (1.93)	.132 (3.35)	
2	.000 (0.00)	.132 (3.35)	
3	.076 (1.93)	.132 (3.35)	
4	.114 (2.90)	.066 (1.68)	
5	.038 (0.97)	.066 (1.68)	
6	.038 (0.97)	.066 (1.68)	
7	.114 (2.90)	.066 (1.68)	
8	.152 (3.86)	.000 (0.00)	
9	.076 (1.93)	.000 (0.00)	
10	.000 (0.00)	.000 (0.00)	
11	.076 (1.93)	.000 (0.00)	
12	.152 (3.86)	.000 (0.00)	
13	.114 (2.90)	.066 (1.68)	
14	.038 (0.97)	.066 (1.68)	
15	.038 (0.97)	.066 (1.68)	
16	.114 (2.90)	.066 (1.68)	
17	.076 (1.93)	.132 (3.35)	
18	.000 (0.00)	.132 (3.35)	
19	.076 (1.93)	.132 (3.35)	

NOTES

1. Location given for each pair is the center of each pair layout.
2. Ground leg location varies within each arrangment. Ground leg location has no impact on pinout. Ground leg stays in same location for both pin and sockets. Pin 1 is indicated (filled) on all arrangements.
3. See AN0005 for proper PCB design rules.
4. Pin cavity and inner contact locations shown. Socket cavity and inner contact locations are mirrored. Ground leg location does not change.

VERSALINK PAIR LAYOUT (SEE NOTE 1)



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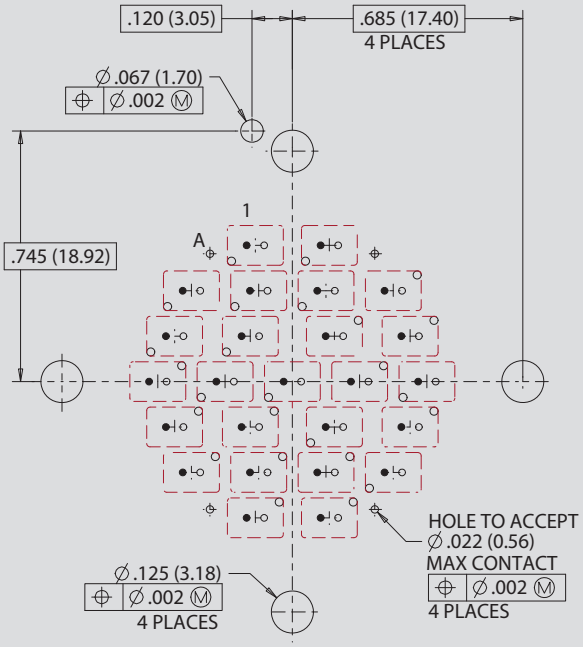
PCB Footprints

Arrangement No. 22-25V4
25X VersaLink
4X 22HD
See Note 1 and 2

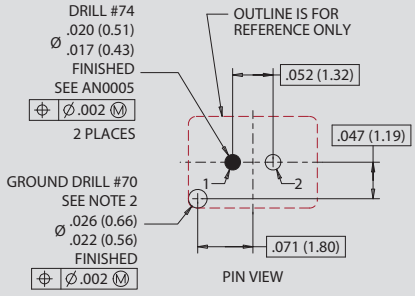


CONTACT POSITIONS		
POS	X	Y
1	-.110 (-2.79)	.405 (10.29)
2	.110 (2.79)	.405 (10.29)
3	-.300 (-7.62)	.270 (6.86)
4	-.100 (-2.54)	.270 (6.86)
5	.100 (2.54)	.270 (6.86)
6	.300 (7.62)	.270 (6.86)
7	-.350 (-8.89)	.135 (3.43)
8	-.125 (-3.18)	.135 (3.43)
9	.125 (3.18)	.135 (3.43)
10	.350 (8.89)	.135 (3.43)
11	-.400 (-10.16)	.000 (0.00)
12	-.200 (-5.08)	.000 (0.00)
13	.000 (0.00)	.000 (0.00)
14	.200 (5.08)	.000 (0.00)
15	.400 (10.16)	.000 (0.00)
16	.350 (8.89)	-.135 (-3.43)
17	-.125 (-3.18)	-.135 (-3.43)
18	.125 (3.18)	-.135 (-3.43)
19	.350 (8.89)	-.135 (-3.43)
20	-.300 (-7.62)	-.270 (-6.86)
21	-.100 (-2.54)	-.270 (-6.86)
22	.100 (2.54)	-.270 (-6.86)
23	.300 (7.62)	-.270 (-6.86)
24	-.110 (-2.79)	-.405 (-10.29)
25	.110 (2.79)	-.405 (-10.29)
A	-.245 (-6.22)	.380 (9.65)
B	.245 (6.22)	.380 (9.65)
C	-.245 (-6.22)	-.380 (-9.65)
D	.245 (6.22)	-.380 (-9.65)

NOTES

1. Location given for each pair is the center of each pair layout.
2. Ground leg location varies within each arrangment. Ground leg location has no impact on pinout. Ground leg stays in same location for both pin and sockets. Pin 1 is indicated (filled) on all arrangements.
3. See AN0005 for proper PCB design rules.
4. Pin cavity and inner contact locations shown. Socket cavity and inner contact locations are mirrored. Ground leg location does not change.

VERSALINK PAIR LAYOUT (SEE NOTE 1)



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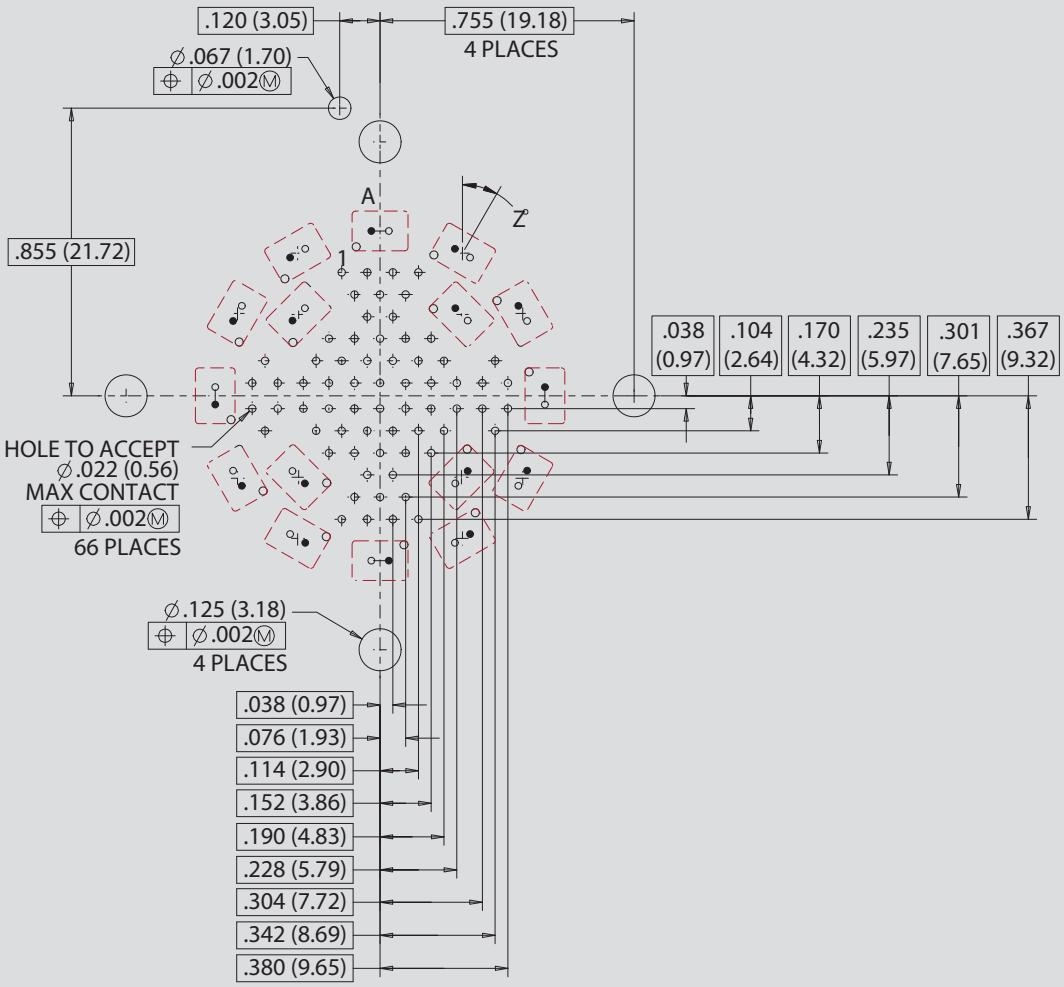
PCB Footprints



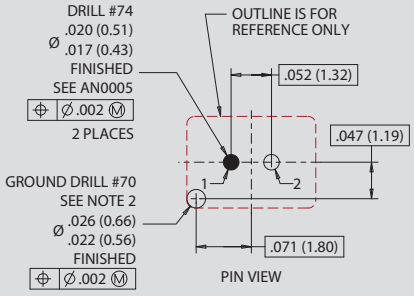
Arrangement No. 24-16V66
16X VersaLink
66X 22HD
See Note 1 and 2

CONTACT POSITIONS			
POS	X	Y	Z ±0.5°
A	.000 (0.00)	.490 (12.45)	0
B	.245 (6.22)	.424 (10.77)	30
C	.424 (10.77)	.245 (6.22)	60
D	.490 (12.45)	.000 (0.00)	90
E	.424 (10.77)	-.245 (-6.22)	120
F	.245 (6.22)	-.424 (-10.77)	150
G	.000 (0.00)	-.490 (-12.45)	180
H	-.245 (-6.22)	-.424 (-10.77)	210
J	-.424 (-10.77)	-.245 (-6.22)	240
K	-.490 (-12.45)	.000 (0.00)	270
M	-.424 (-10.77)	.245 (6.22)	300
N	-.245 (-6.22)	.424 (10.77)	330
P	.42 (10.67)	.242 (6.15)	45
Q	.242 (6.15)	-.242 (-6.15)	135
R	-.242 (-6.15)	-.242 (-6.15)	225
S	-.242 (-6.15)	.242 (6.15)	315

NOTES

1. Location given for each pair is the center of each pair layout.
2. Ground leg location varies within each arrangment. Ground leg location has no impact on pinout. Ground leg stays in same location for both pin and sockets. Pin 1 is indicated (filled) on all arrangements.
3. See AN0005 for proper PCB design rules.
4. Pin cavity and inner contact locations shown. Socket cavity and inner contact locations are mirrored. Ground leg location does not change.

VERSALINK PAIR LAYOUT (SEE NOTE 1)



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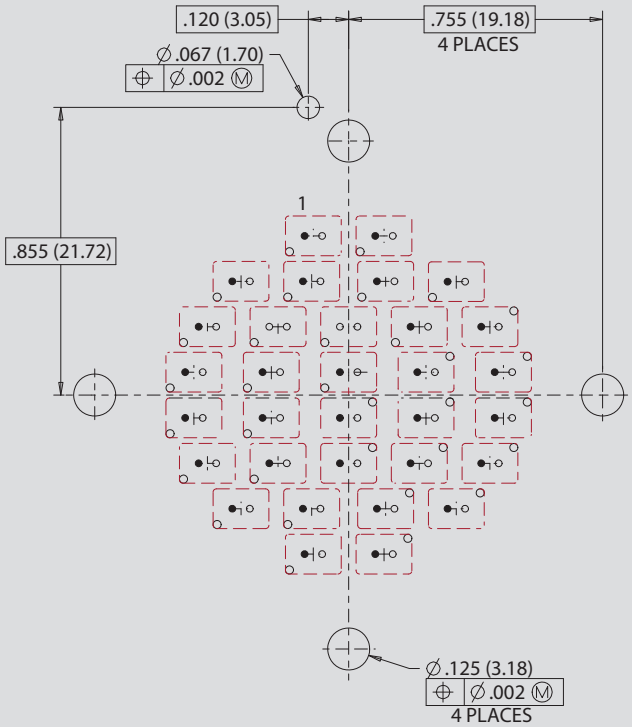
PCB Footprints



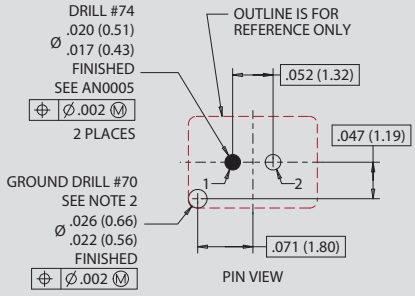
Arrangement No. 24-32V
32X VersaLink
See Note 1 and 2

CONTACT POSITIONS		
POS	X	Y
1	-.105 (-2.67)	.473 (12.01)
2	.105 (2.67)	.473 (12.01)
3	-.320 (-8.13)	.338 (8.59)
4	-.110 (-2.79)	.338 (8.59)
5	.110 (2.79)	.338 (8.59)
6	.320 (8.13)	.338 (8.59)
7	-.420 (-10.67)	.203 (5.16)
8	-.210 (-5.33)	.203 (5.16)
9	.000 (0.00)	.203 (5.16)
10	.210 (5.33)	.203 (5.16)
11	.420 (10.67)	.203 (5.16)
12	-.460 (-11.68)	.068 (1.73)
13	-.230 (-5.84)	.068 (1.73)
14	.000 (0.00)	.068 (1.73)
15	.230 (5.84)	.068 (1.73)
16	.460 (11.68)	.068 (1.73)
17	-.460 (-11.68)	-.068 (-1.73)
18	-.230 (-5.84)	-.068 (-1.73)
19	000 (0.00)	-.068 (-1.73)
20	.230 (5.84)	-.068 (-1.73)
21	.460 (11.68)	-.068 (-1.73)
22	-.420 (-10.67)	-.203 (-5.16)
23	-.210 (-5.33)	-.203 (-5.16)
24	.000 (0.00)	-.203 (-5.16)
25	.210 (5.33)	-.203 (-5.16)
26	.420 (10.67)	-.203 (-5.16)
27	-.230 (-5.84)	-.203 (-5.16)
28	-.110 (-2.79)	-.338 (-8.59)
29	.110 (2.79)	-.338 (-8.59)
30	.320 (8.13)	-.338 (-8.59)
31	-.105 (-2.67)	-.473 (-12.01)
32	.105 (2.67)	-.473 (-12.01)

NOTES

1. Location given for each pair is the center of each pair layout.
2. Ground leg location varies within each arrangment. Ground leg location has no impact on pinout. Ground leg stays in same location for both pin and sockets. Pin 1 is indicated (filled) on all arrangements.
3. See AN0005 for proper PCB design rules.
4. Pin cavity and inner contact locations shown. Socket cavity and inner contact locations are mirrored. Ground leg location does not change.

VERSALINK PAIR LAYOUT (SEE NOTE 1)



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